

ZXMP3F30FH30V SOT23 P-CHANNEL ENHANCEMENT MODE MOSFET

Summary

V _{(BR)DSS (V)}	$R_{DS(on)}$ (Ω)	I _D (A)	
-30	0.080 @ V _{GS} = -10V	-4.0	
	0.140 @ V _{GS} = -4.5V		



Description

This new generation Trench MOSFET from Zetex has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance.

Features

- · Low on-resistance
- · Fast switching speed
- 4.5V gate drive capability
- Thermally enhanced SOT23 package

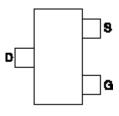
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Applications

- · Power management
- Portable Equipment
- Battery charging

Ordering information

Device	Reel size	Tape width	Quantity	
	(inches)	(mm)	per reel	
ZXMP3F30FHTA	7"	8mm	3,000	



Pinout – top view

Device marking

KPA

Absolute Maximum Ratings

Absolute maximum ratings

Parameter	Symbol	Limit	Unit	
Drain-Source voltage	V_{DSS}	-30	V	
Gate-Source voltage	V_{GS}	±20	V	
Continuous Drain current @ V _{GS} = -10V; T _A =25°C (b)	I _D	-3.4	V	
@ V_{GS} = -10V; T_A =70°C (b)		-2.7		
@ V_{GS} = -10V; T_A =25°C (a)		-2.8		
@ V_{GS} = -10V; T_L =25°C ^(d)		-4.0		
Pulsed Drain current (c)	I _{DM}	-15.3	А	
Continuous Source current (Body diode) (b)	I _S	-2	А	
Pulsed Source current (Body diode) (C)	I _{SM}	-15.3	А	
Power dissipation at T _A =25°C ^(a) Linear derating factor	P _D	0.95 7.6	W mW/°C	
Power dissipation at T _A =25°C ^(b) Linear derating factor	P _D	1.4 11.2	W mW/°C	
Power dissipation at T _L =25°C ^(d) Linear derating factor	P _D	1.96 15.7	W mW/°C	
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C	

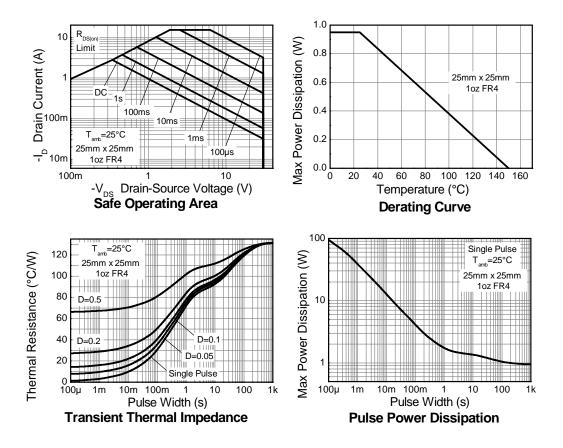
Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient ^(a)	$R_{ heta JA}$	131	°C/W
Junction to ambient ^(b)	$R_{ heta JA}$	89	°C/W
Junction to lead ^(d)	$R_{ heta JL}$	63.77	°C/W

NOTES:

- (a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) Mounted on FR4 PCB measured at t ≤ 10 sec.
 (c) Repetitive rating on 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us pulse width limited by maximum junction temperature.
- (d) Thermal resistance from junction to solder-point (at the end of the drain lead).

Thermal Characteristics



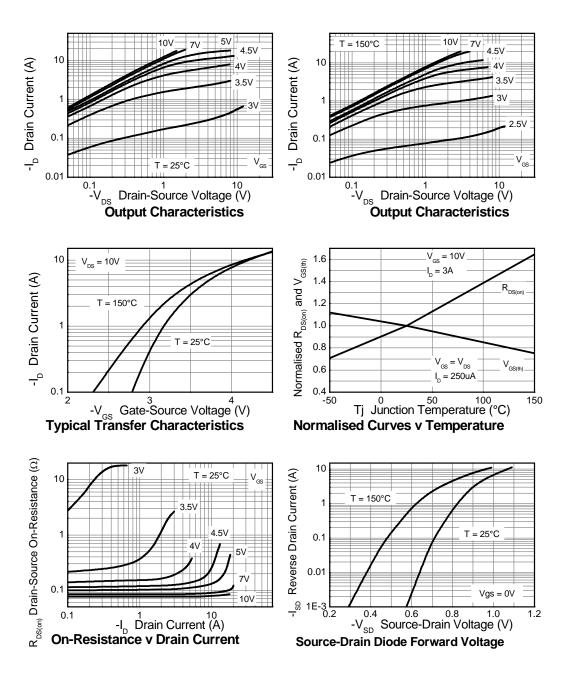
Electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

Parameter	Symb ol	Min.	Тур.	Max.	Unit	Conditions	
Static				•		1	
Drain-Source breakdown voltage	V _{(BR)DSS}	-30			V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate voltage Drain current	I _{DSS}			-1.0	μА	V _{DS} =-30V, V _{GS} =0V	
Gate-Body leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V	
Gate-Source threshold voltage	V _{GS(th)}	-1.0			V	I _D = -250μA, V _{DS} =V _{GS}	
Static Drain-Source on-state resistance (*)	R _{DS(on)}			0.080 0.140	Ω	V _{GS} = -10V, I _D = -2.5A V _{GS} = -4.5V, I _D = -1.9A	
Forward Transconductance (*) (†)	g fs		5		S	V _{DS} = -15V, I _D = -3A	
Dynamic ^(†)							
Input capacitance	C _{iss}		370		pF		
Output capacitance	Coss		72		pF	V_{DS} = -15V, V_{GS} =0V	
Reverse transfer capacitance	C _{rss}		38		pF	f=1MHz	
Switching (‡) (†)							
Turn-on-delay time	t _{d(on)}		1.3		ns		
Rise time	t _r		2.6		ns	V _{DD} = -15V, V _{GS} = -10V	
Turn-off delay time	$t_{d(off)}$		49		ns	I _D = -1A	
Fall time	t _f		22		ns	$R_G \cong 6.0\Omega$,	
Gate charge			1			i	
Total Gate charge	Q_g		7		nC		
Gate-Source charge	Q _{gs}		1.2		nC	V _{DS} = -15V, V _{GS} = -10V	
Gate-Drain charge	Q_{gd}		1.3		nC	1 _D = -3A	
Source-Drain diode	<u>. </u>			•	•		
Diode forward voltage (*)	V_{SD}		-0.80	-1.2	V	I _S = -1.7A,V _{GS} =0V	
Reverse recovery time (‡)	t _{rr}		14.6		ns	I _S = -1.5A,di/dt=100A/μs	
Reverse recovery charge ^(‡)	Q _{rr}		9.5		nC	715 1.3A,αι/αι=100A/μS	

^(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$

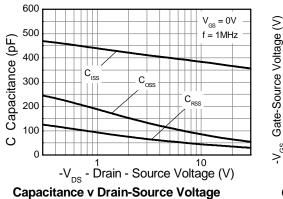
^(†)Switching characteristics are independent of operating junction temperature. (‡)For design aid only, not subject to production testing

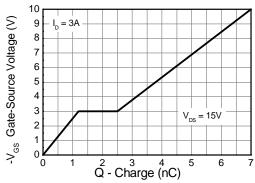
Typical Characteristics



ZXMP3F30FH

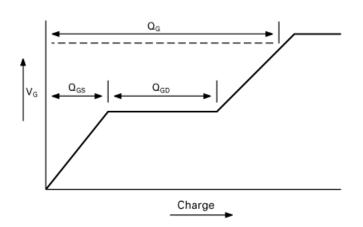
Typical Characteristics





Gate-Source Voltage v Gate Charge

Test Circuits



Current regulator

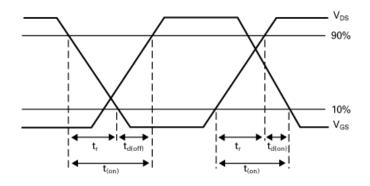
12V 0.2μF 50k Same as D.U.T

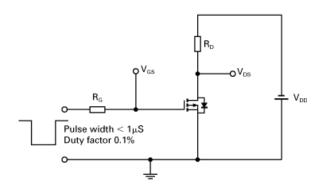
V_{GS}

V_{GS}

Basic gate charge waveform

Gate charge test circuit



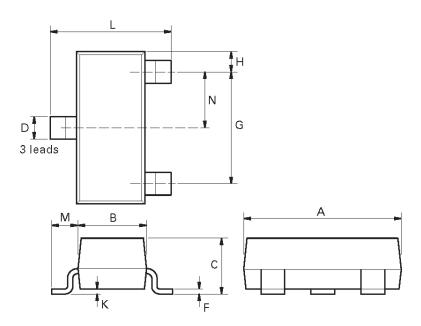


Switching time waveforms

Switching time test circuit

Packaging Details – SOT23

Package outline



Dim.	Millim	Millimeters		Inches		Millim	eters	Inc	hes
	Min.	Max.	Min.	Max.		Min.	Max.	Max.	Max.
Α	2.67	3.05	0.105	0.120	Н	0.33	0.51	0.013	0.020
В	1.20	1.40	0.047	0.055	K	0.01	0.10	0.0004	0.004
С	-	1.10	-	0.043	L	2.10	2.50	0.083	0.0985
D	0.37	0.53	0.015	0.021	М	0.45	0.64	0.018	0.025
F	0.085	0.15	0.0034	0.0059	N	0.95 NOM 0.0375 NO		NOM	
G	1.90 [MOV	0.075	NOM	-	-	-	-	-

 $\textbf{Note:} \ \textbf{Controlling dimensions are in millimeters.} \ \textbf{Approximate dimensions are provided in inches}$

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